

Trends and Challenges in Embedded Systems Research

Christoph Kirsch
Universität Salzburg

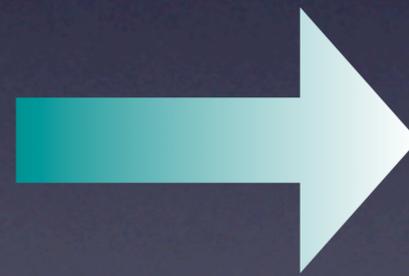
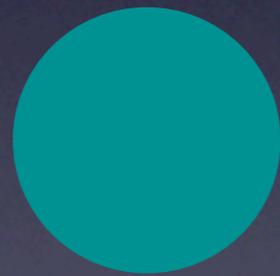


FIT-IT
Vienna, May 2007

Trend

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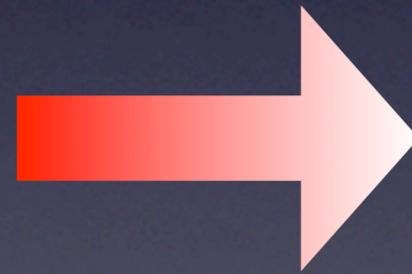
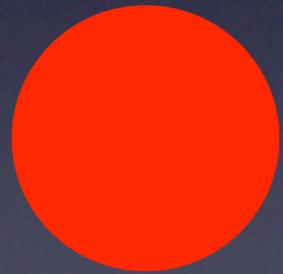
Funding + Policy

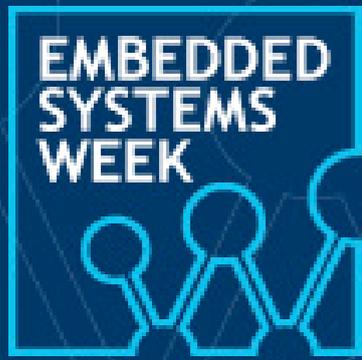


Trend

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Forum + Interest



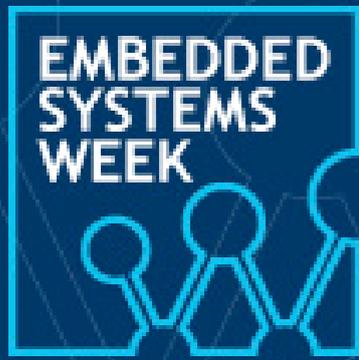


Embedded Systems Week
www.esweek.org

Sept. 30 - Oct. 5, 2007
Salzburg, Austria



- Conferences: CASES, CODES+ISSS, EMSOFT, GPCE, FORMATS



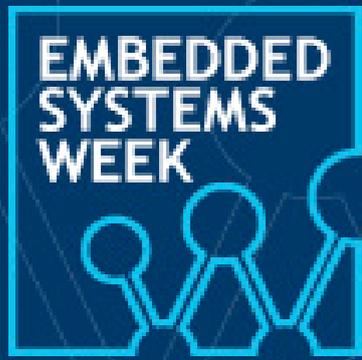
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A photograph of a historic building with a large green dome and spires, likely a church or cathedral in Salzburg, Austria.

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- Workshops (pending): ESTIMedia, WASP, WESS, WESE



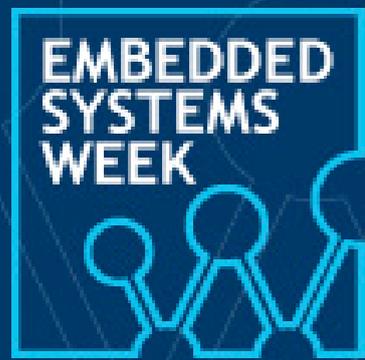
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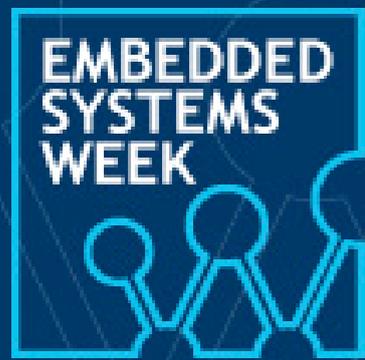
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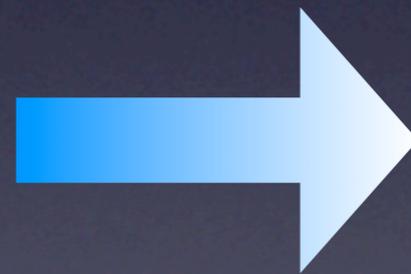


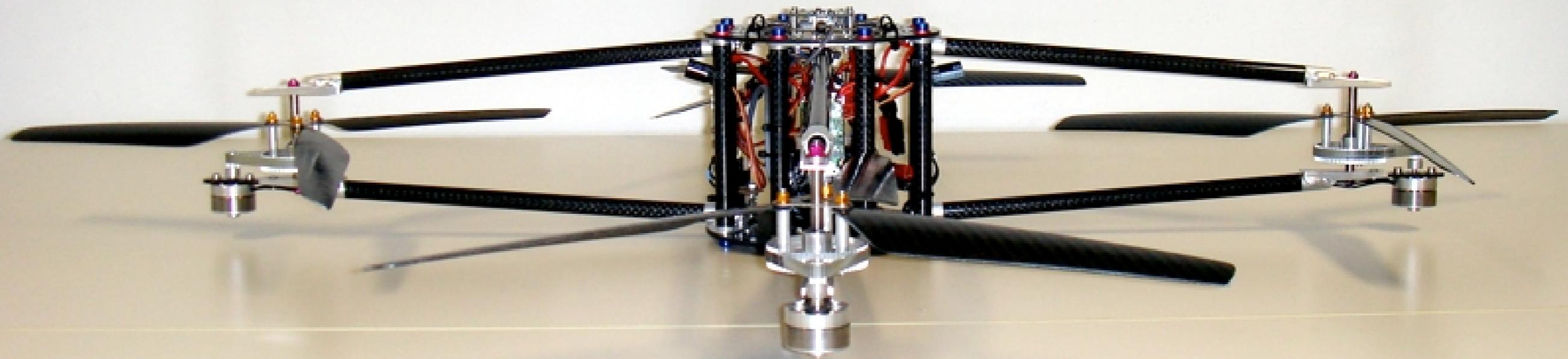
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- Contact: Christoph Kirsch, ck@cs.uni-salzburg.at

Trend

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Challenge + Potential





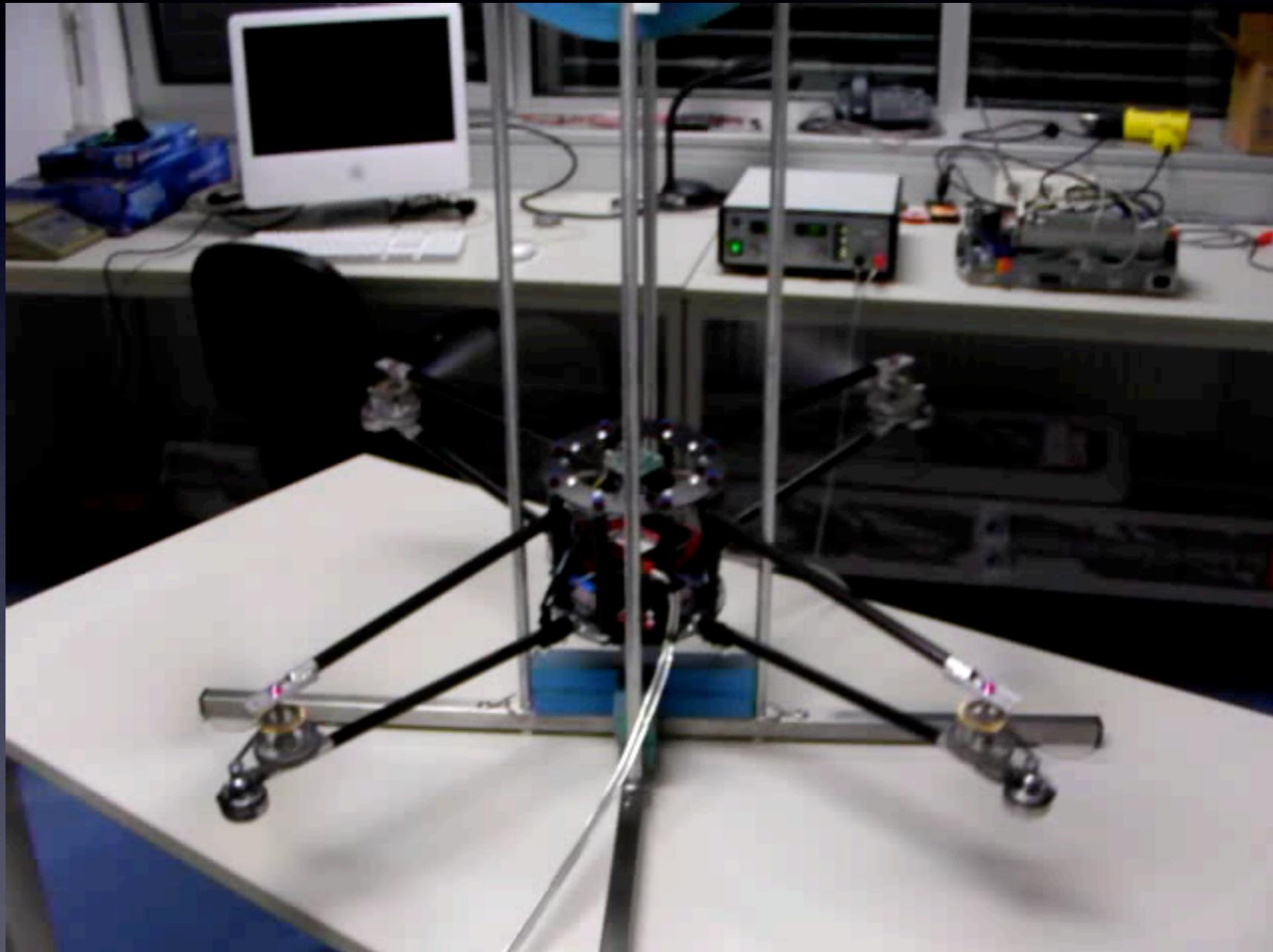
The JAviator

javiator.cs.uni-salzburg.at

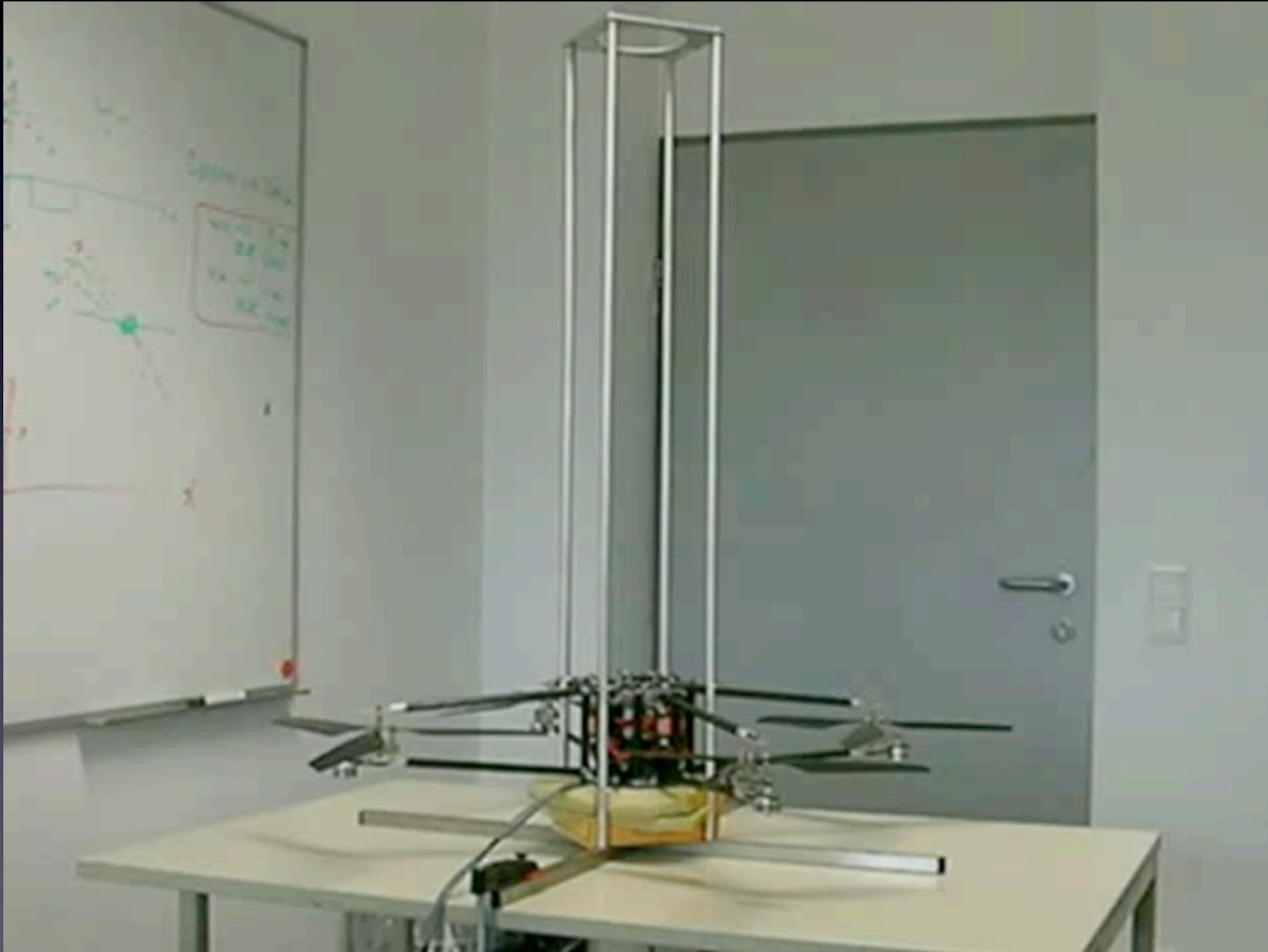
Quad-Rotor Helicopter



Flight Control



Oops

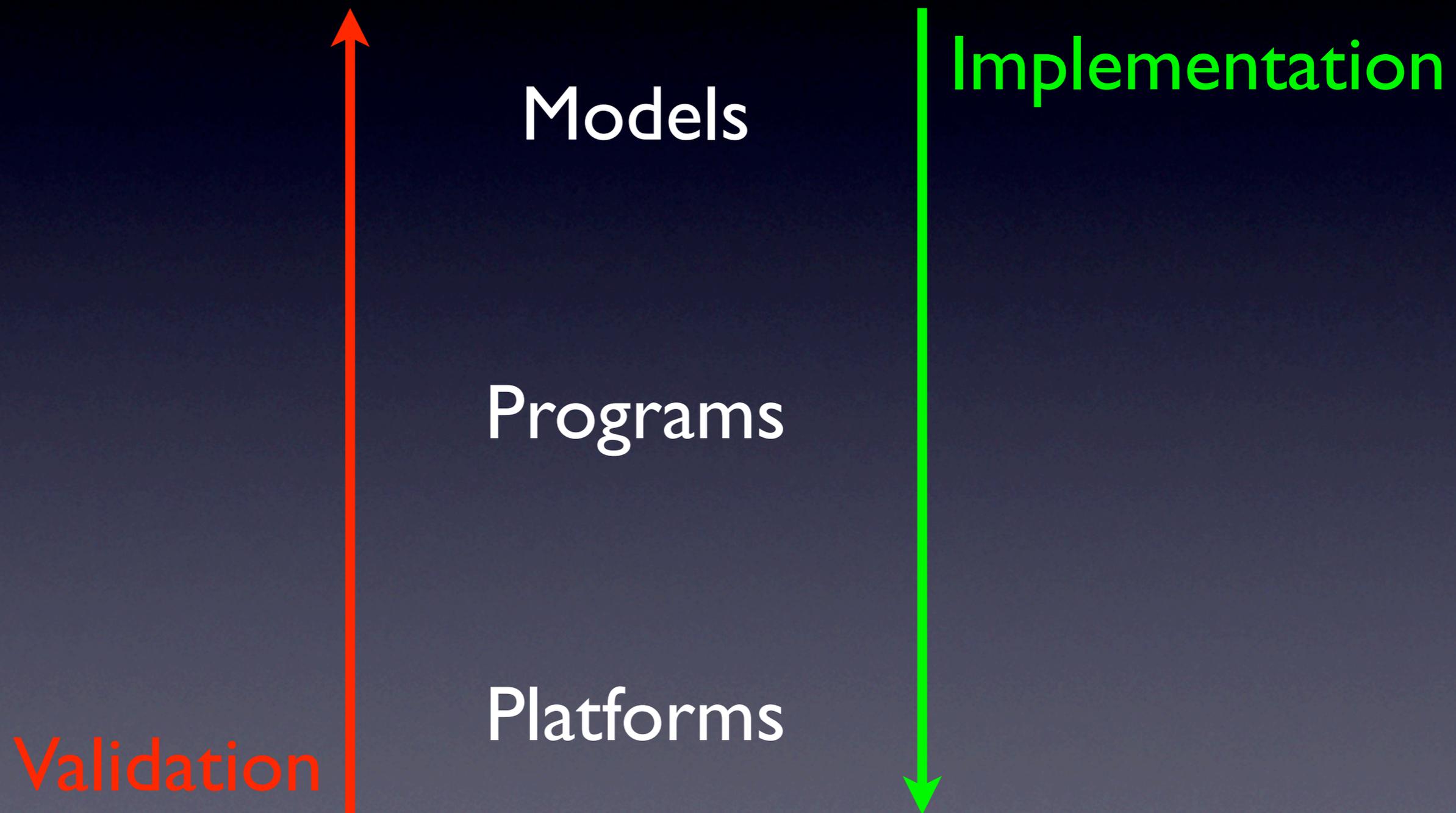


Applications

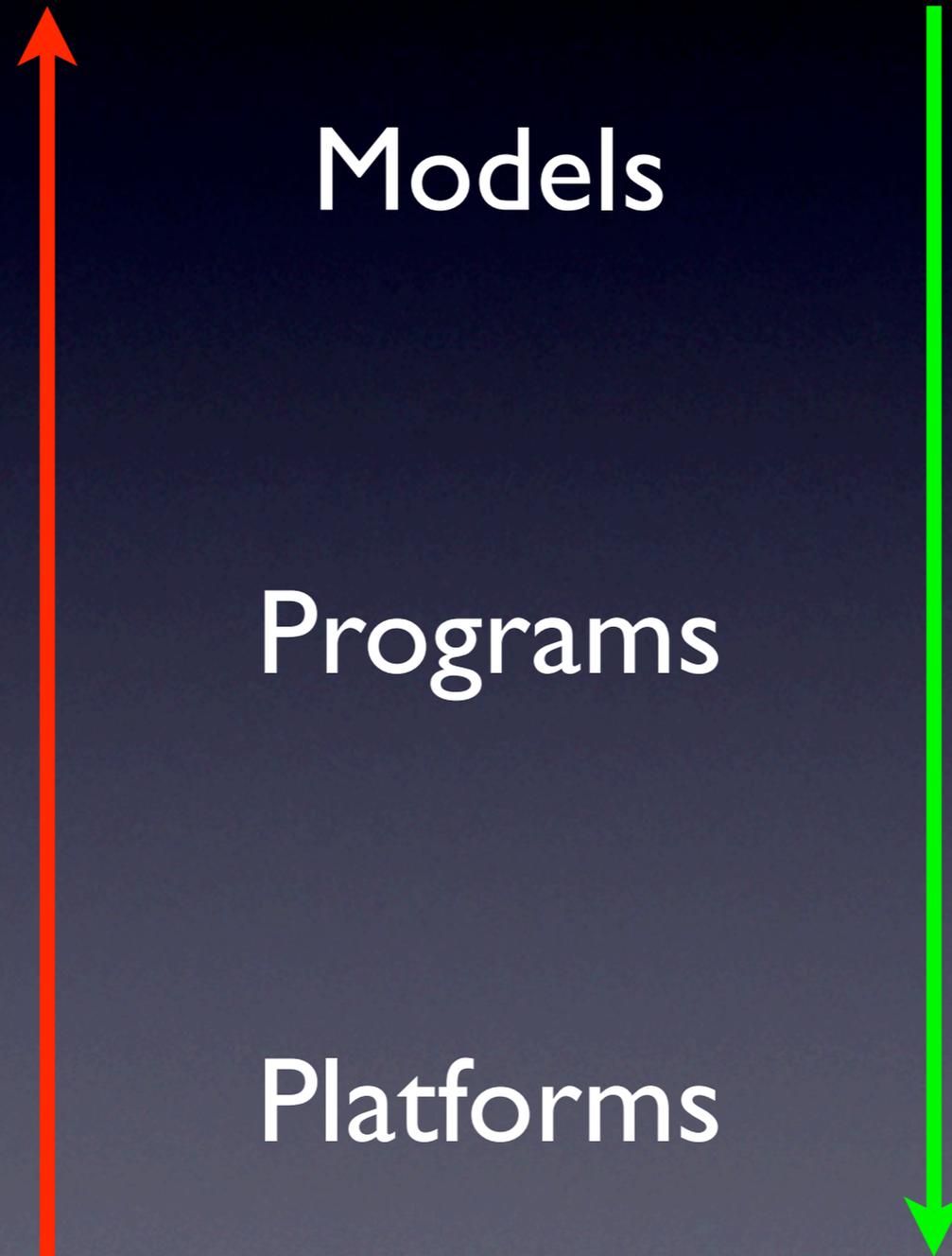
&

Platforms

Engineering Process



Trends & Challenges



Trends & Challenges



Models

Programs

Platforms



Component-Based
Design

Trends & Challenges



Models

Component-Based
Design

Programs

High-Level
Real-Time
Programming

Platforms



Trends & Challenges



Models

Programs

Platforms

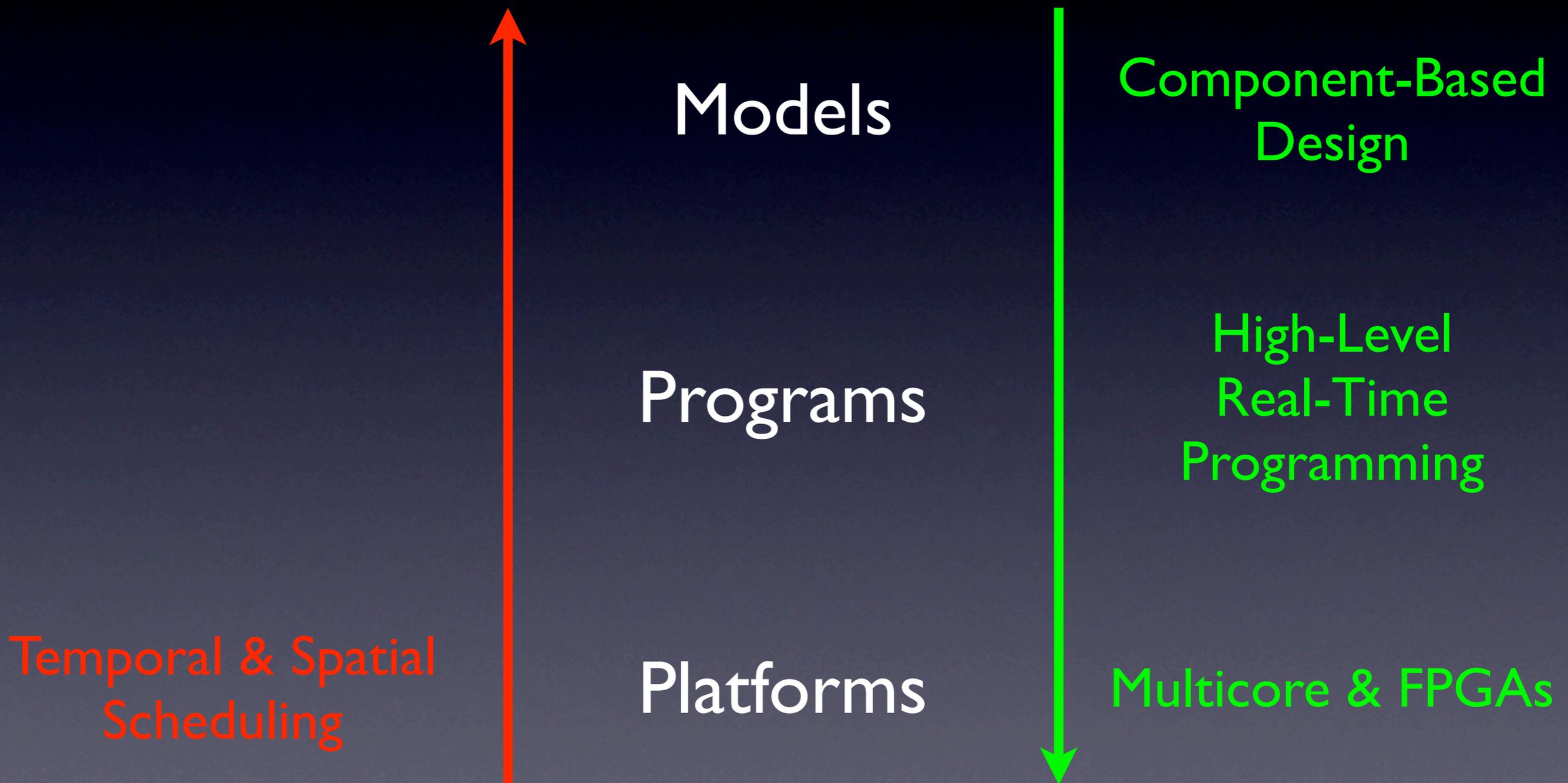


Component-Based
Design

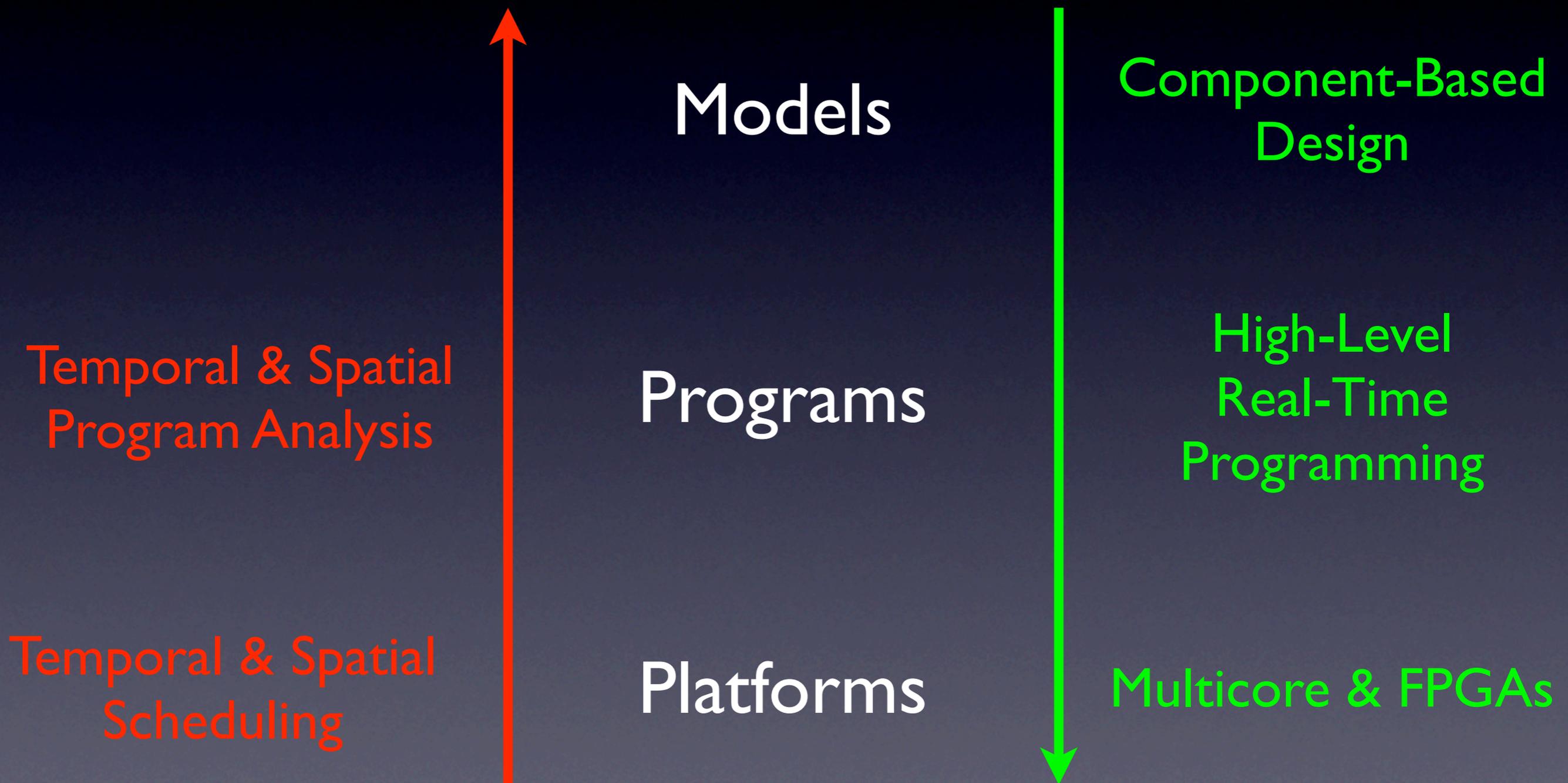
High-Level
Real-Time
Programming

Multicore & FPGAs

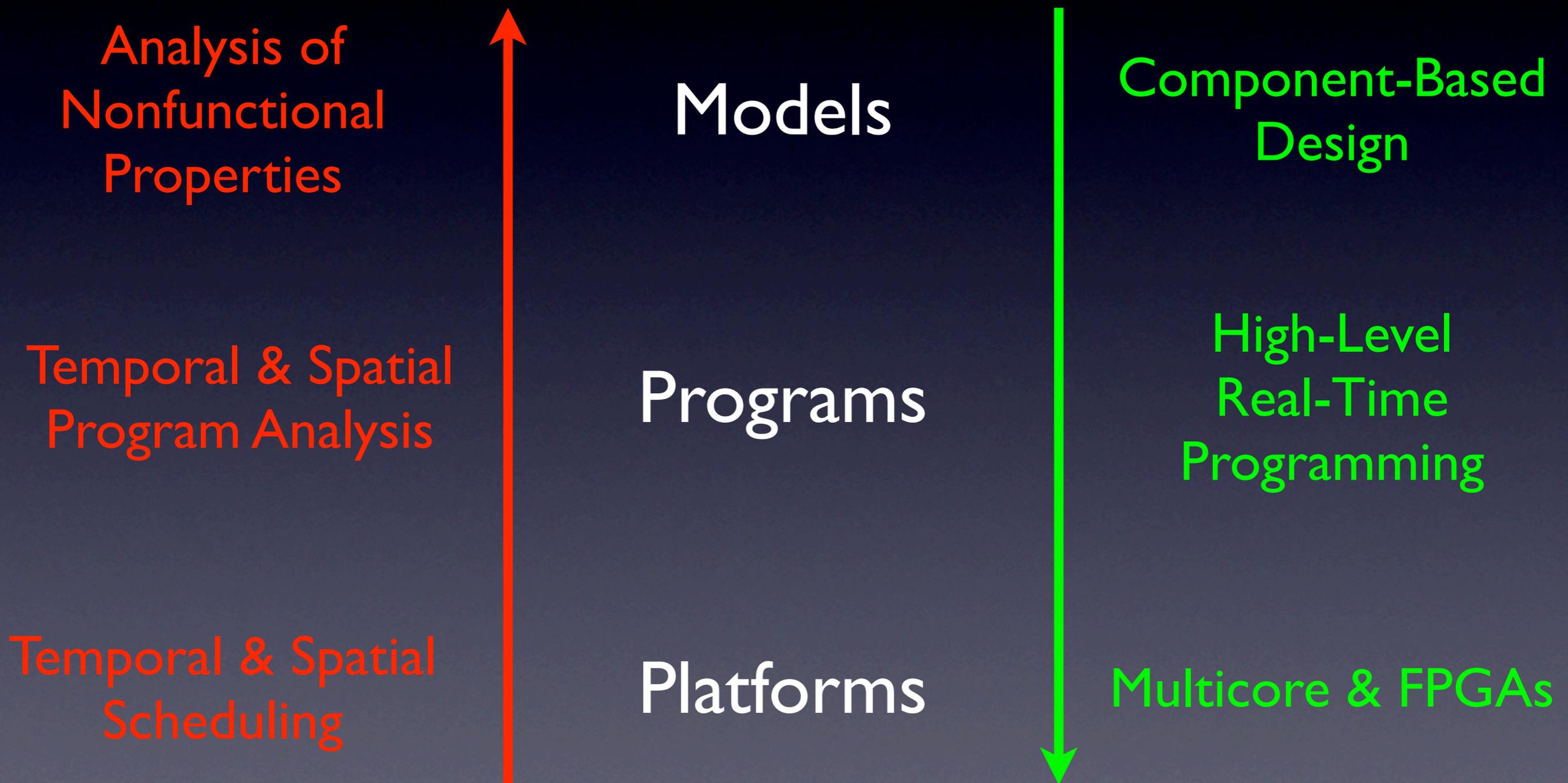
Trends & Challenges



Trends & Challenges



Trends & Challenges



Risk

&

Reputation

Risk

US

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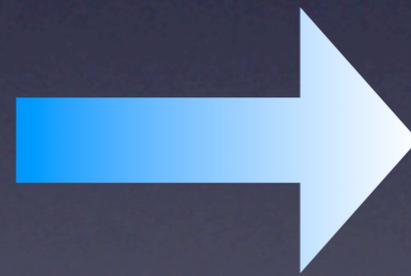
EU

Reputation

Trend

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Challenge + Potential



Thank you