scal.cs.unisalzburg.at

multicore-scalable concurrent data

structures

<u>scalloc.cs.uni-</u> <u>salzburg.at</u>

multicore-scalable concurrent allocator

<u>selfie.cs.uni-</u>

salzburg.at

self-referential systems software for teaching

Scal, Scalloc, and Selfie

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Joint Work

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How do we <u>exchange data</u> among increasingly many cores on a shared memory machine such that <u>performance</u> still <u>increases</u> with the number of cores?

-The Multicore Scalability Challenge



How do we <u>allocate</u> and <u>deallocate</u> shared memory with increasingly many cores such that <u>performance</u> increases with the number of cores while <u>memory consumption</u> stays low?

-Multicore Shared Memory Allocation Problem



~100 cores

How do we teach computer science to students <u>not necessarily</u> <u>majoring in computer science</u> but who anyway <u>code every day</u>?

-The Computer Science Education Challenge



The Multicore Scalability Challenge



number of cores

Timestamped (TS) Stack [POPL15]



Timestamped (TS) Stack [POPL15]



(a) Producer-consumer benchmark, 40-core machine.

(b) Producer-consumer benchmark, 64-core machine.

Elimination Through <u>Shared</u> Timestamps



Figure 6: High-contention producer-consumer benchmark using TS-interval and TS-CAS timestamping with increasing delay on the 40-core machine, exercising 40 producers and 40 consumers. The <u>order</u> in which concurrently pushed elements appear on the TS stack is only <u>determined</u> when they are popped off the TS stack, even if they had been on the TS stack for, say, a year.

-Showing that the TS Stack is a stack is hard hence POPL

Concurrent Data Structures: <u>scal.cs.uni-salzburg.at</u> [POPL13, CF13, POPL15, NETYS15]



Scal: A Benchmarking Suite for Concurrent Data Structures [NETYS15]

Name	Semantics	Year	Ref
Lock-based Singly-linked	strict queue	1968	[1]
Michael Scott (MS) Queue	strict queue	1996	[2]
Flat Combining Queue	strict queue	2010	[3]
Wait-free Queue	strict queue	2012	[4]
Linked Cyclic Ring Queue	strict queue	2013	[5]
Timestamped (TS) Queue	strict queue	2015	[6]
Cooperative TS Queue	strict queue	2015	[7]
Segment Queue	k-relaxed queue	2010	[8]
Random Dequeue (RD)	k-relaxed queue	2010	[8]
Bounded Size k-FIFO	k-relaxed queue, pool	2013	[9]
Unbounded Size k-FIFO	k-relaxed queue, pool	2013	[9]
b-RR Distributed Queue	k-relaxed queue, pool	2013	[10]
Least-Recently-Used (LRU)	k-relaxed queue, pool	2013	[10]
Locally Linearizable DQ	locally linearizable	2015	[11]
Locally Linearizable k-FIFO	locally linearizable	2015	[11]
Relaxed TS Queue	quiescently consistent	2015	[7]
Lock-based Singly-linked	strict stack	1968	[1]
Treiber Stack	strict stack	1986	[12]
Elimination-backoff Stack	strict stack	2004	[13]
Timestamped (TS) Stack	strict stack	2015	[6]
k-Stack	k-relaxed stack	2013	[14]
b-RR Distributed Stack (DS)	k-relaxed stack, pool	2013	[10]
Least-Recently-Used (LRU)	k-relaxed stack, pool	2013	[10]
Locally Linearizable DS	locally linearizable	2015	[11]
Locally Linearizable k-Stack	locally linearizable	2015	[11]
Timestamped (TS) Deque	strict deque	2015	[7]
d-RA DQ and DS	strict pool	2013	[10]



How do we <u>allocate</u> and <u>deallocate</u> shared memory with increasingly many cores such that <u>performance</u> increases with the number of cores while <u>memory consumption</u> stays low?

-Multicore Shared Memory Allocation Problem

Multicore Memory Allocation Problem



number of cores

Scalloc: Concurrent Memory Allocator scalloc.cs.uni-salzburg.at [OOPSLA15]





Local Allocation & Deallocation



Figure 6: Thread-local workload: Threadtest benchmark





"Remote" Deallocation



Figure 9: Temporal and spatial performance for the producer-consumer experiment



Scalloc: Concurrent Memory Allocator scalloc.cs.uni-salzburg.at [OOPSLA15]



Virtual Spans: 64-bit Address Space



Figure 1: Structure of arena, virtual spans, and real spans





Figure 10: Temporal and spatial performance for the object-size robustness experiment at 40 threads

ACDC: Explorative Benchmarking Memory Management acdc.cs.uni-salzburg.at [ISMM13,DLS14]

- configurable multicore-scalable <u>mutator</u> for mimicking virtually any allocation, deallocation, sharing, and access behavior
- written in C, tracks with minimal overhead:
 - 1. memory allocation time
 - 2. memory deallocation time
 - 3. memory consumption
 - 4. memory access time

Memory Access



Figure 11: Memory access time for the locality experiment

How do we teach computer science to students <u>not necessarily</u> <u>majoring in computer science</u> but who anyway <u>code every day</u>?

-The Computer Science Education Challenge



Selfie: Teaching Computer Science [selfie.cs.uni-salzburg.at]

- Selfie is a self-referential 7k-line C implementation (in a single file) of:
 - 1. a <u>self-compiling</u> compiler called *starc* that compiles a tiny subset of C called C Star (C*) to a tiny subset of MIPS32 called MIPSter,
 - 2. a <u>self-executing</u> emulator called *mipster* that executes MIPSter code including itself when compiled with starc,
 - 3. a <u>self-hosting</u> hypervisor called *hypster* that virtualizes mipster and can host all of selfie including itself, and
 - 4. a tiny C* library called *libcstar* utilized by all of selfie.

5 statements: assignment while if return procedure()



pointer arithmetics c = * (s+i);

return n;

library: exit, malloc, open, read, write

> make cc -w -m32 -D'main(a,b)=main(a,char**argv)' selfie.c -o selfie

-bootstrapping selfie using standard C compiler



-selfie usage



–compiling selfie with selfie (takes seconds)

> ./selfie -c selfie.c -m 2 -c selfie.c

./selfie: this is selfie's starc compiling selfie.c

./selfie: this is selfie's mipster executing selfie.c with 2MB of physical memory

selfie.c: this is selfie's starc compiling selfie.c

selfie.c: exiting with exit code 0 and 1.05MB of mallocated memory

./selfie: this is selfie's mipster terminating selfie.c with exit code 0 and 1.16MB of mapped memory

–compiling selfie with selfie <u>and</u> then running that executable to compile selfie again (takes ~6 minutes)

> ./selfie -c selfie.c -o selfie1.m -m 2 -c selfie.c -o selfie2.m

./selfie: this is selfie's starc compiling selfie.c
./selfie: 121660 bytes with 28779 instructions and 6544 bytes of data
written into selfie1.m

./selfie: this is selfie's mipster executing selfie1.m with 2MB of physical memory

selfiel.m: this is selfie's starc compiling selfie.c
selfiel.m: 121660 bytes with 28779 instructions and 6544 bytes of data
written into selfie2.m

selfie1.m: exiting with exit code 0 and 1.05MB of mallocated memory

./selfie: this is selfie's mipster terminating selfiel.m with exit code 0 and 1.16MB of mapped memory

> -compiling selfie with selfie <u>and</u> generating an executable **selfie1.m** that is then executed to compile selfie again generating another executable **selfie2.m** (takes ~6 minutes)

> ./selfie -c selfie.c -m 2 -c selfie.c -m 2 -c selfie.c

-compiling selfie with selfie <u>and</u> then running that executable to compile selfie again <u>and</u> then running that executable to compile selfie again (**takes ~24 hours**)

> ./selfie -c selfie.c -m 2 -c selfie.c -y 2 -c selfie.c

-compiling selfie with selfie <u>and</u> then running that executable to compile selfie again <u>and</u> then **hosting** that executable in a virtual machine to compile selfie again (**takes ~12 minutes**)





